



Analog Design Engineer

- **Position:** Full-time, salaried employee
- **Location:** Cambridge, MA
- **Requirements:** MSEE with 7+ years, or PhD EE 5+ years of IC design experience
- **Start Date:** Flexible (from April 15, 2011)
- **Salary:** Negotiable; Dependent on experience
- **Company Description:**

OnChip Power is a fast-paced, VC-backed start-up poised to disrupt the power supply industry. We are developing a new class of power supply systems based on a novel VHF switching architecture. We are seeking an experienced analog IC design engineer to join our team.

- **Job Responsibilities:**

The analog design engineer will take a lead in the specification and design of a variety of power management circuit blocks including oscillators, drivers, controllers, and interface circuits.

Your primary responsibility will be the design of analog, mixed-signal circuits in power IC processes technology (primarily BCD). You will participate in key technical decision and design reviews, and will be responsible for circuit analysis, design, simulation, layout, verification, and testing.

Background with high voltage processes and knowledge of EMC, packaging, testing, and thermal management is desired.

You must be familiar with the Cadence analog IC design environment including Spectre, Layout-XL, parasitic extraction tools (QRC) and DV tools, in addition HSPICE and MATLAB skills will be necessary. Knowledge of Synopsys digital flow and analog validation tools a plus.

- **To apply:**

Applications will be accepted on a rolling basis.

Please submit a cover letter and resume to: [careers at onchippower dot com](mailto:careers@onchippower.com)